

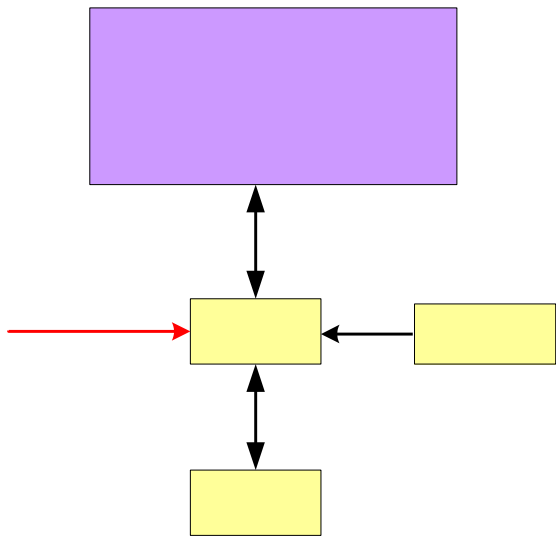
ARM X86

WOL Magic Packet

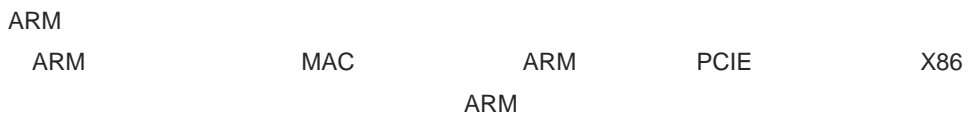
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1	1
2	1
2.1	X86	1
2.2	ARM	2
2.2.1	2
3	4
4	5
5	5

2-1 X86



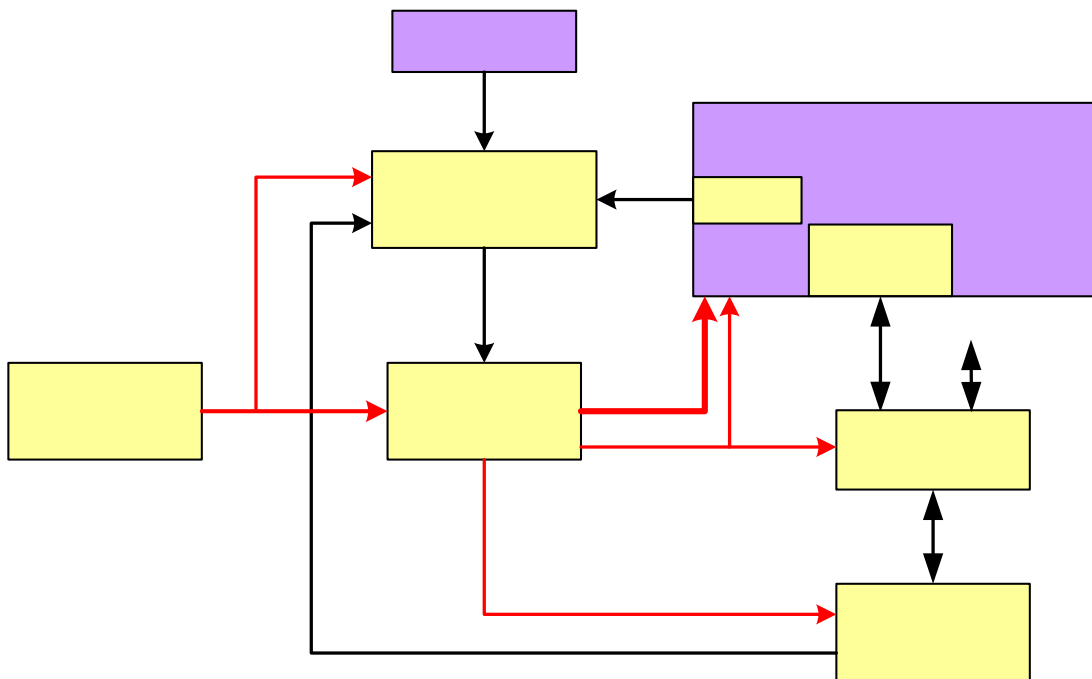
2.2 ARM



2.2.1



2-2 ARM



CPU
 VCC_3.3V_STB DC_IN
 PHY MAC DMI SMI PHY CPU
 VCC_3.3V_STB VCC_3.3V VCC_3.3V 0
 PHY DMI/SMI CPU VCC_3.3V 3.3V
 DMI
 SMI PHY PHY MAC
 ARM
 2-3

DC

CPU

PHY

PHY

